

JCWSCS 2 4 MAR 2004

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March 11, 2004

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FAX NO.: (703) 308-7751

NO. OF PAGES: 5

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FROM: Bonnie McClarren
Secretary to Kevin P. Radigan, Esq.
Heslin Rothenberg Farley & Mesiti P.C.

RE: Serial No.: 10/727,856
Filing Date: 12/04/03
Our File No.: 0406.241A

Message: Please see attached Communication Requesting Correction Of Official Filing Receipt. After corrections have been made, please issue a corrected Official Filing Receipt.

Thank you.

THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Ference et al.

Confirmation No.: 7934

Serial No.: 10/727,856

Group Art Unit: 3726

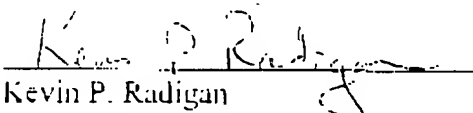
Filed: 12/04/03

Examiner: Unknown

Title: MULTI-CHIP STACK AND METHOD OF FABRICATION UTILIZING
SELF-ALIGNING ELECTRICAL CONTACT ARRAY

Certificate of Facsimile Transmission

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11, 2004.


Kevin P. Radigan
Attorney for Applicants
Registration No. 31,789

Date of Signature: March 11, 2004.

To: Assistant Commissioner for Patents
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**COMMUNICATION REQUESTING CORRECTION
OF OFFICIAL FILING RECEIPT**

Sir:

Upon our review of the Official Filing Receipt for the above-identified application, we
noted the following error:

BUR919990304US2

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INCORRECT TITLE


MULTI-CHIP STACK AND METHOD OF FABRICATION UTILIZING SELF-ALIGNING ELECTRICAL CONTACT ARRAY

CORRECT TITLE

MULTI-CHIP STACK AND METHOD OF FABRICATION UTILIZING SELF-ALIGNING ELECTRICAL CONTACT ARRAY

A copy of the Filing Receipt is enclosed herein with the noted corrections. Applicants hereby request that a corrected Official Filing Receipt indicating the above-referenced corrections be issued.

Respectfully submitted,


Kevin P. Radigan
Attorney for Applicants
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Dated: March 11, 2004

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APPL NO	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/727,856	12/04/2003	3726	770	BUR919990304US2	6	15	1

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03/05/04
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CONFIRMATION NO. 7934

FILING RECEIPT



0000000012035012

Date Mailed: 03/05/2004

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Filing Receipt Corrections, facsimile number 703-746-9195. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

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Assignment For Published Patent Application

International Business Machines Corporation, Armonk, NY;

Domestic Priority data as claimed by applicant

This application is a DIV of 09/732,240 12/07/2000

Foreign Applications

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If Required, Foreign Filing License Granted: 03/04/2004

Projected Publication Date: 06/10/2004

Non-Publication Request: No

Early Publication Request: No

MAR - 8 2004

Title

Multi-chip package and method of fabrication utilizing self-aligning electrical contact array

- 5/20/04 -

Preliminary Class
029

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Title 35, United States Code, Section 184
Title 37, Code of Federal Regulations, 5.11 & 5.15

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